

Q: How To Enable Frequency Selection and Forbid RB

odi Commands:

odi -n duapp0 enable-ulrb-forbidden 0

Disable frequency selection switch

odi -n duapp0 enable-ulrb-forbidden 1

Enable frequency selection switch

odi -n duapp0 enable-ulrb-forbidden

Query frequency selection switch Status

odi -n duapp0 show-forbidden-map

Query how many RBs forbidden

Example:

Enable Frequency selection and forbid RBs Starts 0 to 60 at TTI 18:

odi -n duapp0 set-forbidden-map 18 0 60 1

18: Stands for TTI, Range (-1~19), -1 indicates all TTIs of Uplink configuration

0: Indicates start RBs of forbidden, range (0~272)

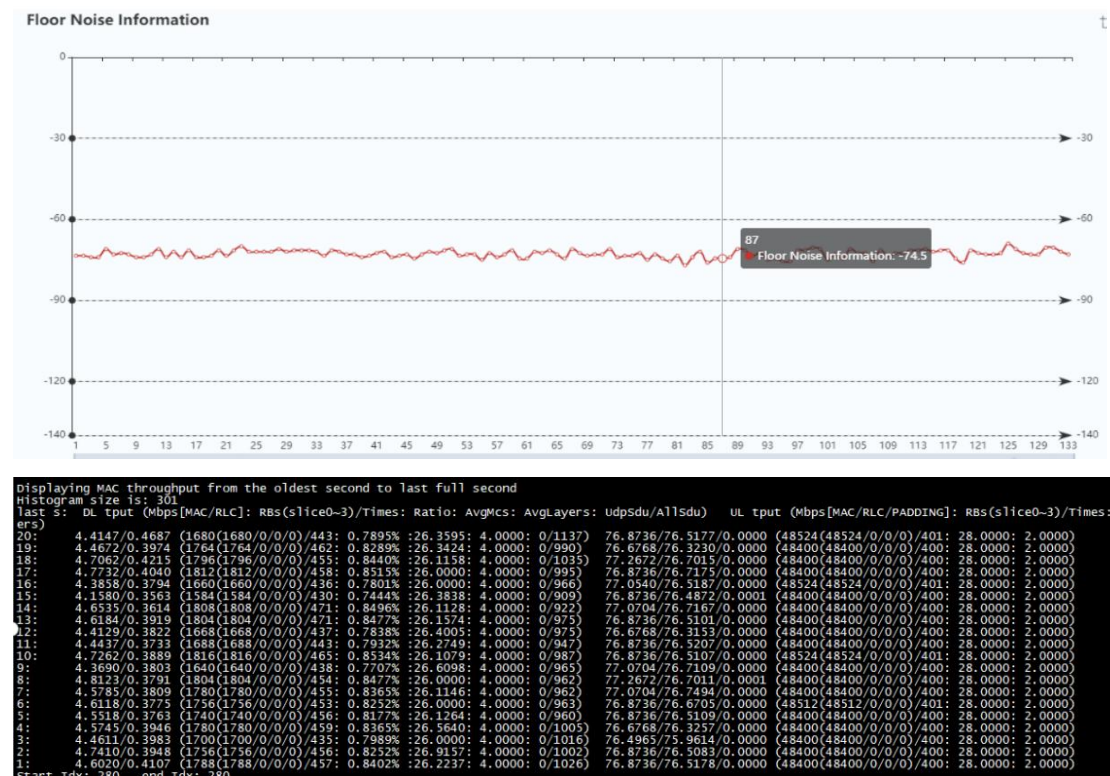
60: Indicates end RBs of forbidden, range (0~272)

1: Indicates TTI and forbidden RBs effective or not, range (1: positive, 0: negative)

Case 1: Add Interference for front 10M bandwidth

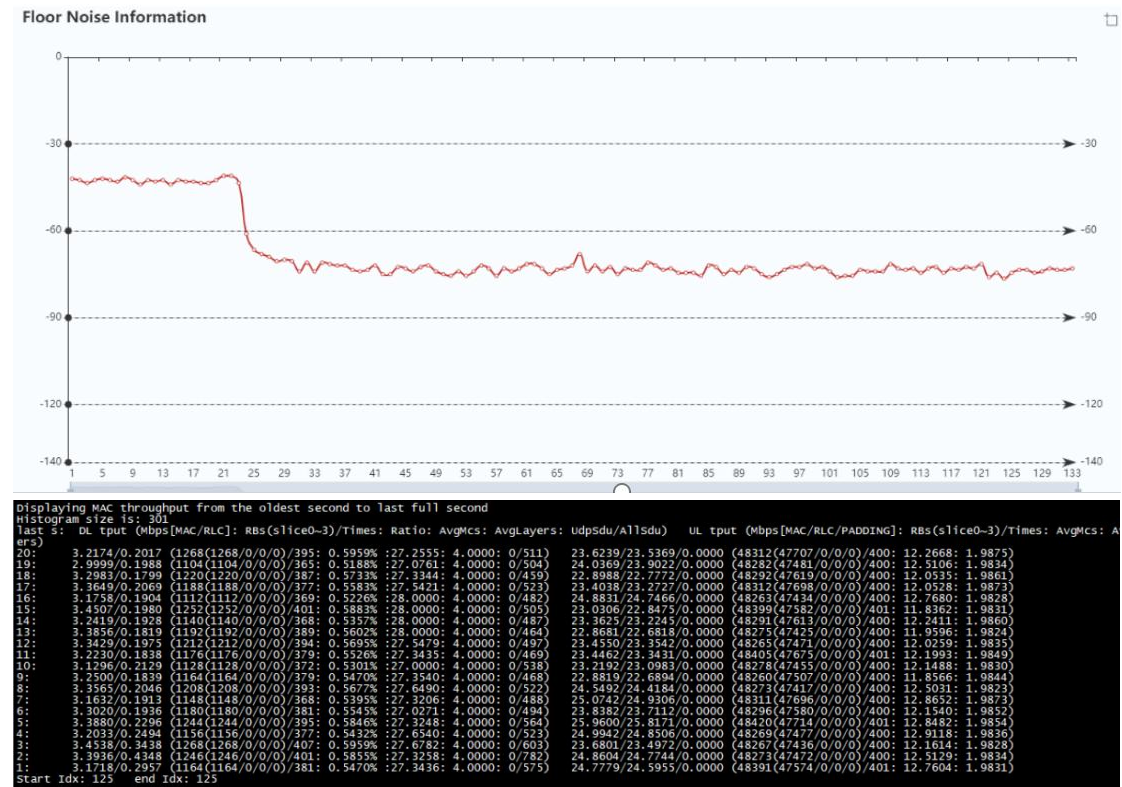
Step 1: Target Node A: Used frequency range 3625Mhz~3675Mhz, 7D2U.

Floor Noise and UL throughput without interference of target node A:



Step 2: Add Interference Source Node B: Used frequency 3585~3635Mhz, 3D1U, Frame Offset: 3ms

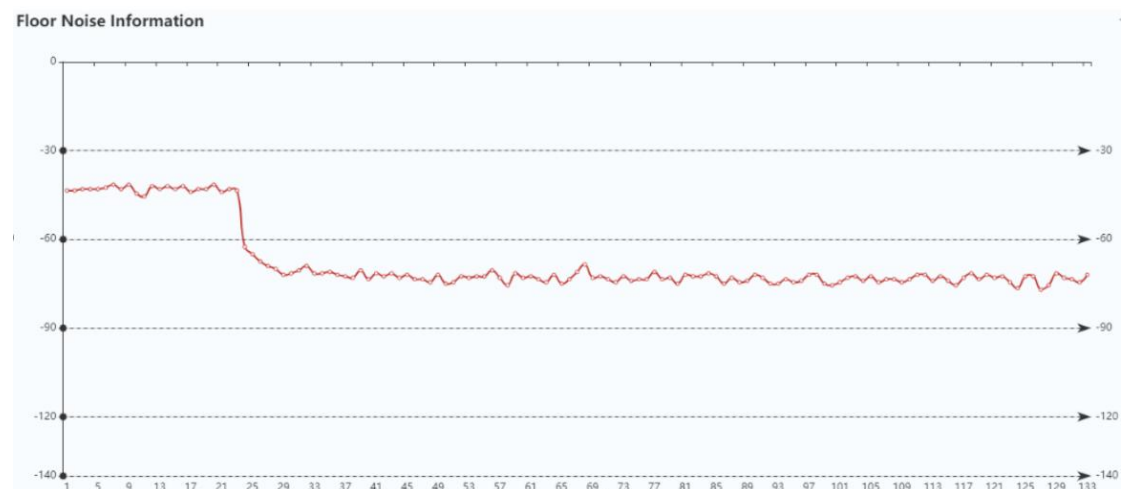
Floor Noise and UL throughput with interference of target node A:



Step 3 :Enable Frequency selection and forbid RBs of front 10M bandwidth for target node A

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odi -n duapp0 enable-ulrb-forbidden 1
odi -n duapp0 set-forbidden-map 18 0 26 1
odi -n duapp0 set-forbidden-map 19 0 26 1
odi -n duapp0 set-forbidden-map 8 0 26 1
odi -n duapp0 set-forbidden-map 9 0 26 1
```

Floor noise and UL throughput for target node A:





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displaying MAC throughput from the oldest second to last full second
Histogram size is: 301
last s: DL tput (Mbps[MAC/RLC]: RBs(slice0-3)/Times: Ratio: AvgMcs: AvgLayers: UdpSdu/AllSdu) UL tput (Mbps[MAC/RLC/PADDING]: RBs(slice0-3)/Times: AvgMcs:
ers)
20: 3.1722/0.3588 (1204(1204/0/0/0)/383: 0.5658% :26.3355: 4.0000: 0/900) 54.5553/54.2984/0.0000 (40066(39427/0/0/0)/400: 24.6473: 1.9841)
19: 3.0236/0.3316 (1144(1144/0/0/0)/373: 0.5376% :26.0699: 4.0000: 0/838) 52.2598/51.9938/0.0000 (40074(39298/0/0/0)/400: 24.6844: 1.9806)
18: 3.1233/0.3357 (1196(1196/0/0/0)/384: 0.5620% :26.2709: 4.0000: 0/855) 53.0488/52.8005/0.0000 (40062(39356/0/0/0)/400: 24.6612: 1.9824)
17: 3.1115/0.3414 (1140(1140/0/0/0)/363: 0.5357% :26.7263: 4.0000: 0/871) 54.3155/54.0432/0.0000 (40152(39574/0/0/0)/401: 24.6775: 1.9856)
16: 3.4615/0.3438 (1284(1284/0/0/0)/402: 0.6034% :27.6106: 4.0000: 0/877) 54.2460/53.9880/0.0000 (40061(39448/0/0/0)/400: 24.9640: 1.9847)
15: 3.1307/0.3638 (1188(1188/0/0/0)/380: 0.5583% :26.2492: 4.0000: 0/928) 56.3020/55.8809/0.0000 (40066(39379/0/0/0)/400: 25.0075: 1.9829)
14: 3.1885/0.3659 (1180(1180/0/0/0)/381: 0.5545% :26.7356: 4.0000: 0/905) 56.3737/56.2423/0.0000 (40075(39246/0/0/0)/400: 26.2961: 1.9818)
13: 3.1880/0.3646 (1164(1164/0/0/0)/373: 0.5470% :27.0000: 4.0000: 0/930) 57.2207/56.9288/0.0000 (40148(39496/0/0/0)/401: 26.0103: 1.9838)
12: 3.1726/0.3728 (1152(1152/0/0/0)/369: 0.5414% :27.0000: 4.0000: 0/950) 57.3206/57.0601/0.0000 (40052(39411/0/0/0)/400: 25.9456: 1.9840)
11: 3.2083/0.3552 (1156(1156/0/0/0)/376: 0.5432% :27.0000: 4.0000: 0/906) 57.0896/56.7986/0.0000 (40081(39312/0/0/0)/400: 25.9393: 1.9810)
10: 3.1608/0.3766 (1176(1176/0/0/0)/378: 0.5526% :27.3469: 4.0000: 0/920) 56.6633/56.2614/0.0000 (40061(39361/0/0/0)/400: 26.3717: 1.9825)
9: 3.2310/0.3876 (1180(1180/0/0/0)/383: 0.5545% :27.0000: 4.0000: 0/950) 54.7981/54.5375/0.0000 (40049(39268/0/0/0)/400: 25.9121: 1.9805)
8: 3.2060/0.3285 (1220(1220/0/0/0)/392: 0.5733% :26.4131: 4.0000: 0/838) 51.8619/51.6084/0.0000 (40074(39331/0/0/0)/400: 24.1426: 1.9815)
7: 3.2392/0.3234 (1224(1224/0/0/0)/391: 0.5752% :27.2124: 4.0000: 0/825) 53.1144/52.8484/0.0000 (40172(39464/0/0/0)/401: 24.3735: 1.9824)
6: 3.2599/0.3921 (1180(1180/0/0/0)/369: 0.5545% :27.0542: 4.0000: 0/949) 54.1373/53.8812/0.0000 (40047(39553/0/0/0)/400: 24.8734: 1.9877)
5: 3.2550/0.3701 (1216(1216/0/0/0)/390: 0.5714% :27.3322: 4.0000: 0/904) 53.4344/53.0338/0.0000 (40054(39328/0/0/0)/400: 24.8684: 1.9819)
4: 3.1844/0.3310 (1168(1168/0/0/0)/377: 0.5489% :27.2568: 4.0000: 0/839) 53.6043/53.2042/0.0000 (40071(39337/0/0/0)/400: 24.7169: 1.9817)
3: 3.3975/0.3652 (1240(1240/0/0/0)/396: 0.5827% :27.0806: 4.0000: 0/930) 56.4133/56.2734/0.0000 (40181(39434/0/0/0)/401: 25.3533: 1.9814)
2: 3.4354/0.3764 (1260(1260/0/0/0)/395: 0.5921% :27.3175: 4.0000: 0/954) 59.1711/58.8810/0.0000 (40068(39454/0/0/0)/400: 26.6214: 1.9847)
1: 3.4205/0.3642 (1252(1252/0/0/0)/404: 0.5883% :27.3227: 4.0000: 0/929) 57.2710/56.9975/0.0000 (40080(39249/0/0/0)/400: 26.1367: 1.9793)
CPU tot. tdy: 96.0000 CPU med. tdy: 96.0000
```

Compare: NO interference, interference for front 10M,interference for front 10M(Enable frequency selection and forbid RBs)

	No Interference	Add interference for front 10M	Add interference for front 10M (Enable UL Frequency Selection and Forbid RBs)
Floor Noise	-70	-43	-43
MCS	28	12	25
RB Grant	400	400	400
RSRP	-65	-65	-65
SINR	36	13	12
UL Throughput (Mbps)	76	23	54