

# **UDP DL Low Throughput**

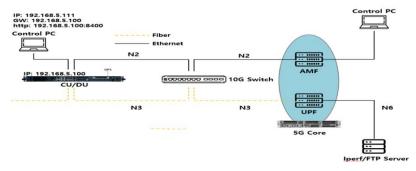
Country/Region: A/K, T

Equipment: nCell-T5000 +EU+RU4240

**Version:** BBU: 5GNR\_ax.tdd.fr1.2.2.3\_422\_r50381\_i19\_20221028\_085555+EU:

 $V0.05.05 + RRU: V0.05.05 + Acceleration\ Card:\ FGAF2.2.0\_220811\_R$ 

### Networking:



### 1. Issue Description:

Run Iperf test from Core server after network setup done and UE registered, both TCP and UDP Downlink speed is limited at around 500Mbps, PDSCH schedule times is low.

Iperf test for DL TCP:

Iperf test for DL UDP:

```
odi: tx 47 bytes to port 46974 on 192.0.2.1

Jisplaying MC throughput from the oldest second to last full second

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```

2. Suggested Troubleshooting Methods:

Check why the RB dispatch is low:

- a. Check if the radio environment good.
- b. Check if signed AMBR and QCI is restricted.
- c. Check if income traffic insufficient.
- d. Check if MAC/RLC/PDCP Layer issue
- 3. Troubleshooting Procedures:



Padding traffic at air interface, it can reach more than 800Mbps and the scheduled times is full.

Check the AMBR and QCI of UE, maximum 5Gbps and 5QI-9, no restriction.

```
value
```

UEAggregateMaximumBitRate

uEAggregateMaximumBitRateDL: 5000000000bits/s uEAggregateMaximumBitRateUL: 5000000000bits/s

- QosFlowSetupRequestList: 1 item
  - ✓ Item 0
    - QosFlowSetupRequestItem qosFlowIdentifier: 1
      - qosFlowLevelQosParameters
        - ▼ qosCharacteristics: nonDynamic5QI (0)
          - ▼ nonDynamic5QI

fiveQI: 9

Check the Tx/Rx speed rate of interface in Iperf server and gNB, it shows the income traffic is sufficient and

Iperf server N6/N3 interface:

gNB N3 Interface:



Check the PDCP layer throughput, it's same with MAC layer. Check the user-plane data and see there is a lot UDP data stacking which is not extracted, suspect CPU core assignment improperly.

```
0 127.0.0.1:21.

0 0.0.0.0:33057

0 0.0.0.0:2152

192.0.2.2:3006
                                                                                                                                   127.0.0.1:27149
0.0.0.0:*
                                                                                                                                                                                                            ESTABLISHED 108564/cpupproxy
                                                                                                                                                                                                                                                 108564/cpupproxy
107718/upapp
108564/cpupproxy
107718/upapp
108564/cpupproxy
108564/cpupproxy
                                                                                                                                    0.0.0.0:*
0.0.0.0:*
0.0.0.0:*
0.0.0.0:*
                  268436864
udp
udp
udp
udp
                                      0 192.0.2.2:3006

0 0.0.0.0:56093

0 0.0.0.0:27103

0 0.0.0.0:27130

0 127.0.0.1:27147

0 127.0.0.1:27147

0 0.0.0:43748

0 :::2152

[ ACC ] STREAM
                                 0 0 0
                                                                                                                                                                                                                                               108564/cpupproxy
107718/upapp
107718/upapp
108564/cpupproxy
107718/upapp
public/cleanup
/run/systemd/cgroups-agent
/tmp/pdcp_ctrl_msg_SRC.sock
public/pickup
 udp
udp
udp
                                                                                                                                    0.0.0.0:*
0.0.0.0:*
                                                                                                                                                      21059 2192/master
1318 1/systemd
30249 108564/cpupproxy
21055 2192/master
62413027 107718/upapp
                                                                         STREAM
DGRAM
                                                                          DGRAM
STREAM
DGRAM
                                                                                                            LISTENING
                                                                                                                                                                                                                                                  /tmp/pdcp_ctrl_msg_dst.sock
                                                                           DGRAM
DGRAM
                                                                                                                                                       30241 108564/cpupproxy
61899943 91665/pickup
```

Check the Core Assignment files (CoreAssignment.conf , protStackCfg.sh , dataplane\_env) under /opt/bbu/oam/cm, it's not correct. Modify those files as per below:

Modify "CoreAssignment.conf"

```
# nformation for LTE thread
     # CoreId[n]=m menas core m is assigned to thread n
     # the m should be assigned in asending order
     CoreNumForLteRt = 14; # number of core for LTE RT thread
     # 11d use 1, 25,27
     # core id for LTE RT thread
     CoreId[0] = 3;
     CoreId[1] = 4;
     CoreId[2] = 5;
     CoreId[3] = 6;
     CoreId[4]
     CoreId[5]
               = 8;
     CoreId[6] = 9;
     CoreId[7]
               = 17;
     CoreId[8] = 18;
     CoreId[9] = 19;
     CoreId[10] = 20;
     CoreId[11] = 21;
     CoreId[12] = 22;
     CoreId[13] = 23;
     {/LteCpuInfo}
     # CPU information for Front end interface
     {FeIfCpuInfo->}
29
     CoreNumForIf = 1; # number of core for front end interface
     # core id for front end interface
30
     CoreId[0] = 1;
     {/FeIfCpuInfo}
33
34
     # CPU information for hardware control
     CoreNumForHwCtrl = 4; # number of core for LDPC hardware control
     # core id for hardware control
     CoreId[0] = 2:
     CoreId[1] = 2;
     CoreId[2] = 2;
40
     CoreId[3] = 2;
41
42
     {/HwCtrlCpuInfo}
43
44
     # CPU information for CPRI card interface
45
     {CpriCtrlCpuInfo->}
46
     CoreNumForCpriCtrl = 0; # number of core for cpri card control
47
     # core id for hardware control
48
     # try one virtual core
49
     CoreId[0] = 6;
50
     CoreId[1] = 8;
51
     CoreId[2] = 10;
     # same physical core as bbuio
53
     #CoreId[0] = 30:
```



## Modify "dataplane\_env"

```
#
# If no hugepages reserved, then ...
       OFP_2M_SZ_HUGEPAGES_RSV=2048
        export OFP_2M_SZ_HUGEPAGES_RSV
        #
# CPU resources
       # Put available cores here cpus=(14 12 13 26 27)
       #CPU core assignment
       # UP Cores
PDCP_SYS_TIMER_CORE=${cpus[0]}
BH_IO_DISPATCH_CORE=${cpus[1]}
BH_WORKER_CORE="**{cpus[2]}, *{cpus[4]}"
BH_CORE_MASK=0x03000000
       BH_WORKER_QDISC=1
        export PDCP_SYS_TIMER_CORE BH_IO_DISPATCH_CORE BH_WORKER_CORE BH_CORE_MASK BH_WORKER_QDISC
       # OFP PKT I/O Cores
OFP_BH_PKIO_CORE="${cpus[1]},${cpus[2]}"
       export OFP_BH_PKIO_CORE
       # OFP APP Cores
#OFP_APP_CORE=${cpus[0]}"
       #export OFP_BH_APP_CORE
       # FCH aysnc
BH_FCH_ASYNC_IO_CORE=${cpus[3]}
       export BH FCH ASYNC IO CORE
        # Comment the two lines out, and use internal NGU IP if vGateway is enabled. 
OFP_IPADDR=10.88.130.92/24
OFP_HOP=1
# Internal NGU IP while vGateway is enabled.
```

#### Modify "protStackCfg.sh"

```
oamprocess_filter=7
odsnameserver_filter=7
gnb_filter=7
gnb_filter=7
gpb_filter=7
g
```



Restart protocol stack and disable the switch of security and integrity, restart UE and do iperf test again, dispatch is full and both tcp&udp throughput improved.

```
odi: dest processor is: 102.0.2.1
odi: tx 47 bytes to port 32008 on 102.0.2.1
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Displaying Mich Chrushpyth from the oldest second to last full second

Histogram size is: 301
last s: Di tut (Mbps[MaC/RLC]: RBs(slice0-3)/Times: AvgMcs: AvgLayers: UdpSdu/AllSdu)
Odi: 726.7523/722.1040 (375027/07/09/0 /1401: 08.1232* 116.0903: 4.0000: 0/66352)
odi: 726.7523/722.1040 (375027/07/09/0 /1401: 08.1232* 116.1804: 4.0000: 0/66352)
odi: 702.8238/989.7508 (375122/07/09/0 /1401: 08.1818* 110.2875: 4.0000: 0/653212
odi: 702.8238/989.7508 (375122/07/09/0 /1401: 08.1818* 110.2875: 4.0000: 0/653212
odi: 702.8238/989.7508 (375122/07/09/0 /1401: 08.00000* 1.00000: 0/653212
odi: 702.8238/989.7508 (375122/07/09/0 /1401: 08.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000* 1.00000
```

### 4. Conclusion:

- a. Security and integrity impacts throughput, need be disabled.
- b. CPU core assignment issue impact RB dispatch, related core files (CoreAssignment.conf, protStackCfg.sh, dataplane\_env)need be aligned as per latest solution.